NALLA NARASIMHA REDDY
Education Society's Group of Institutions – Integrated Campus
Approved by AICTE, New Delhi, Affiliated to JNTU – Hyderabad
SCHOOL OF ENGINEERING

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Name of the Subject : Computer Organization and Architecture   **Subject Code : CS304PC**

Year/Sem   **: II-I**   **Regulation**   **: R18**

CO1	Ability to Demonstrate an understanding of the design of the functional units of a digital computer system
CO2	Ability to design of control unit and Explain the instruction set, instruction formats and
CO3	Addressing modes of CPU
CO4	Ability to Recognize and manipulate representations of numbers stored in digital computers and perform Basic arithmetic Operations.
CO5	Ability to analyze memory hierarchy and its impact on computer Cost/performance.

Mapping Matrix of CO's and PO's:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10	P11	P12	PSO1	PSO2
CO1	X		X		X									
CO2	X	X	X	X	X									
CO3	X				X									
CO4	X	X		X										
CO5	X		X											

Course
Coordinator

Program
Coordinator

HOD